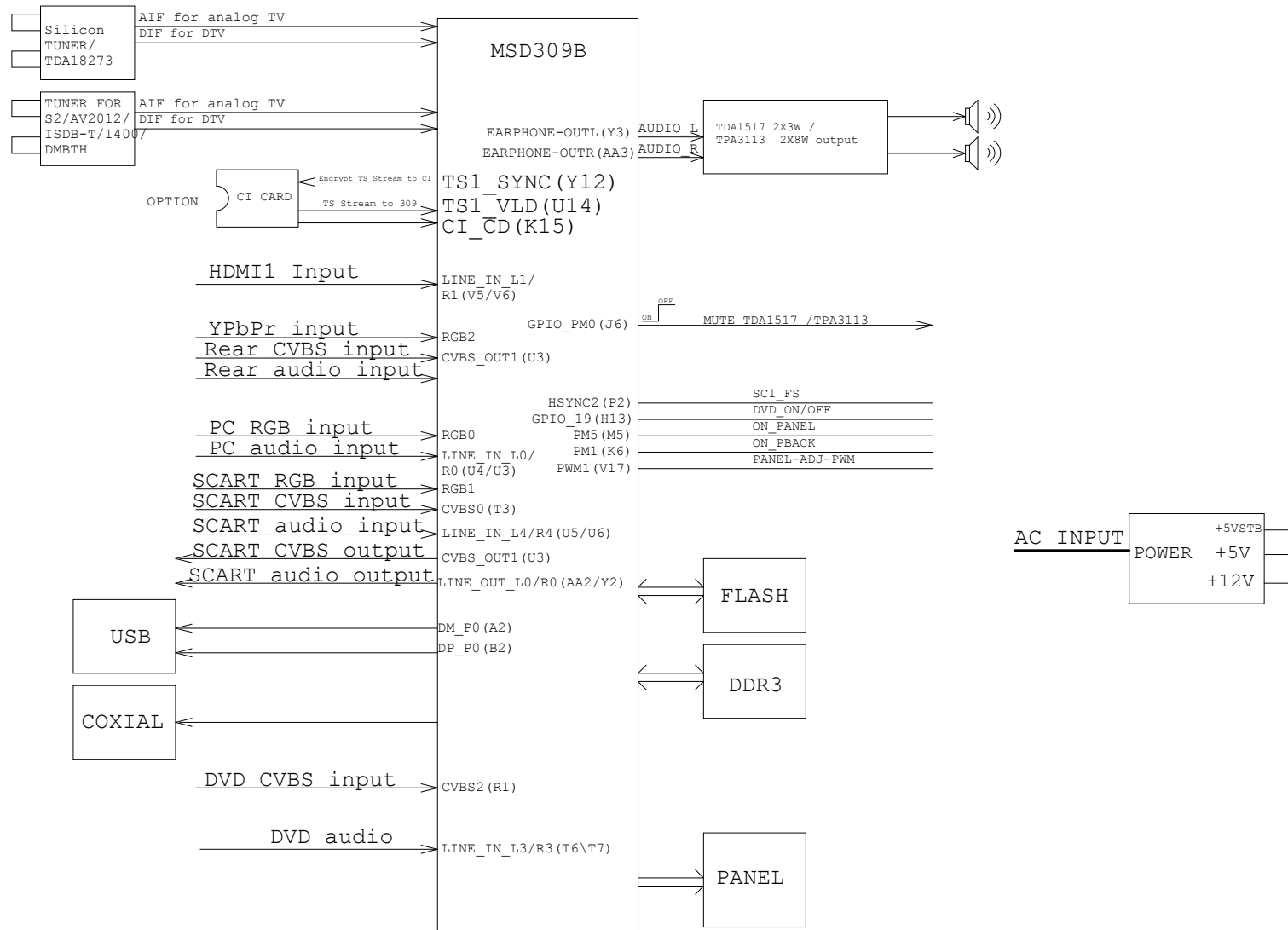


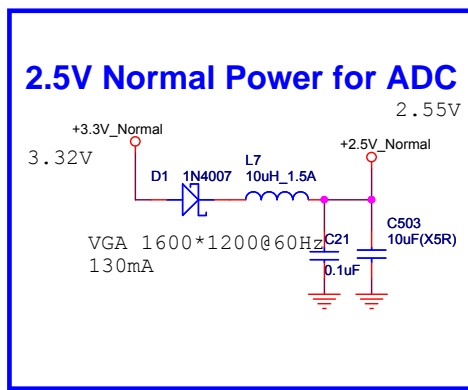
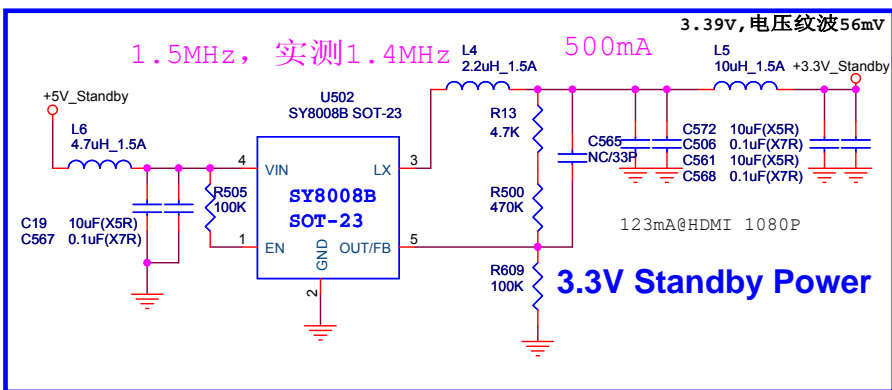
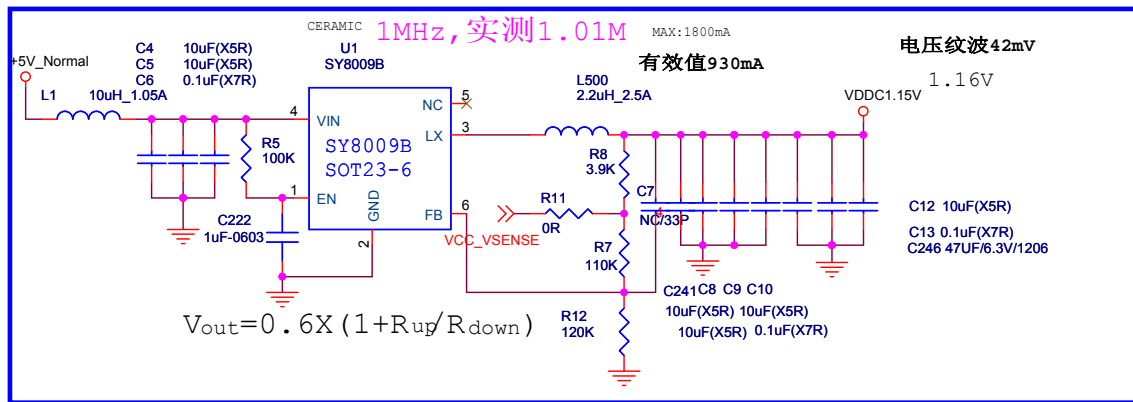
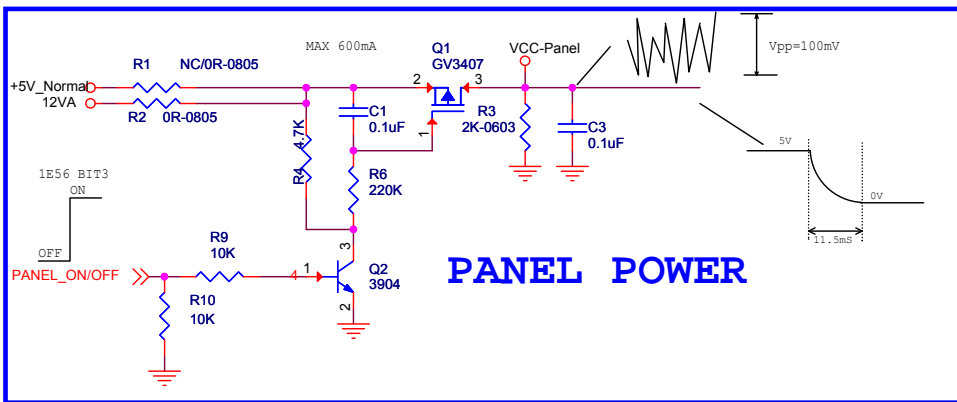
chang list:

增加MHL1201功能 (2012.12.12)  
只留epin flash封装 (2012.12.12)  
背光电容c1026交换正负极 (2012.12.12)  
R826由0402封装换为0603封装 (2012.12.12)  
增加R63  
MHL 1.2V供电去系统core电压 (2012.12.12)  
DVD的IR连接到MUC GPIO (M21) Pin (2012.12.12)

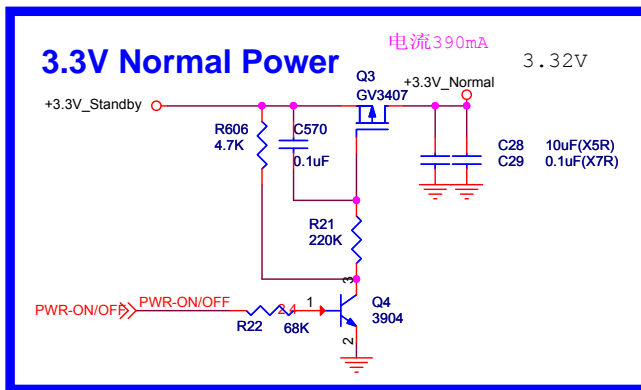
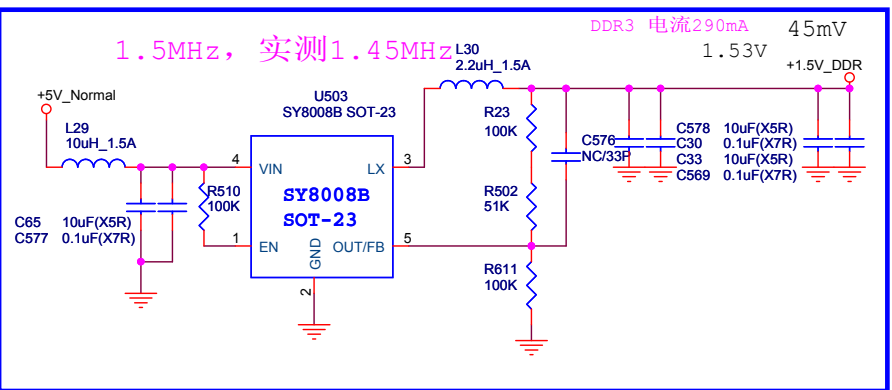
## SCH BLOCK FOR MSD309B





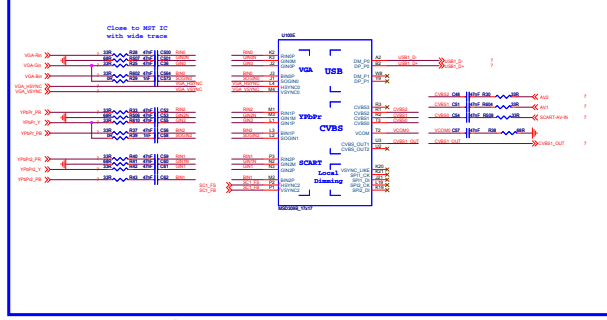


**1.5V Normal Power for DDR3**

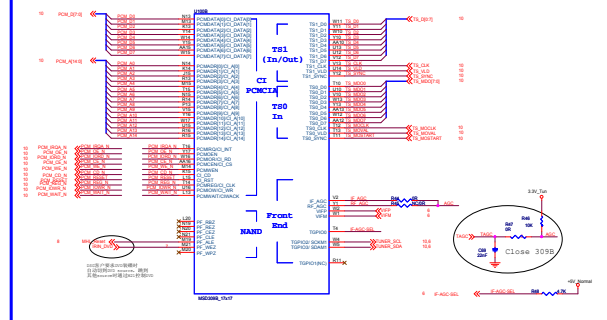


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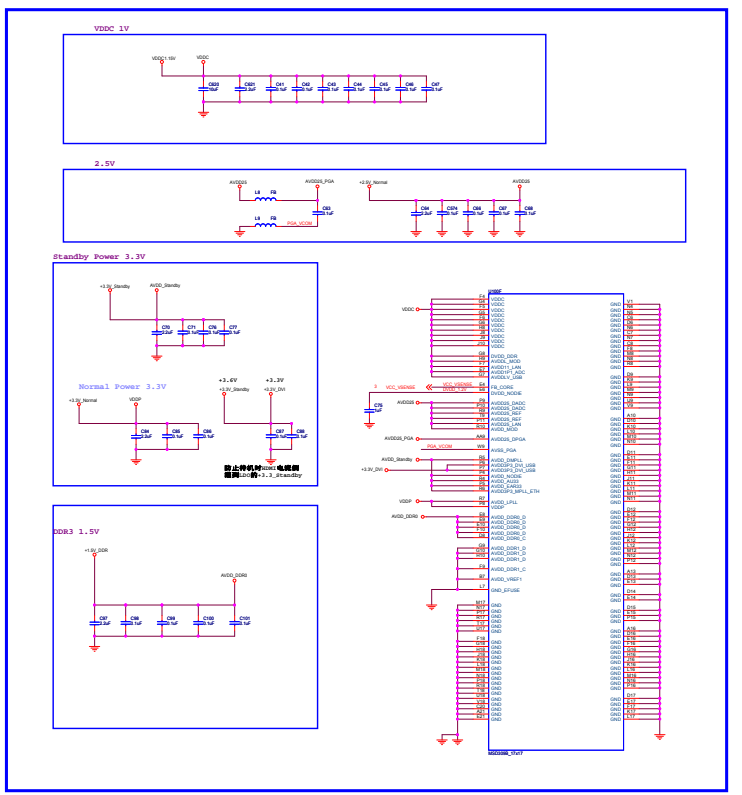
### RGB & CVBS & USB



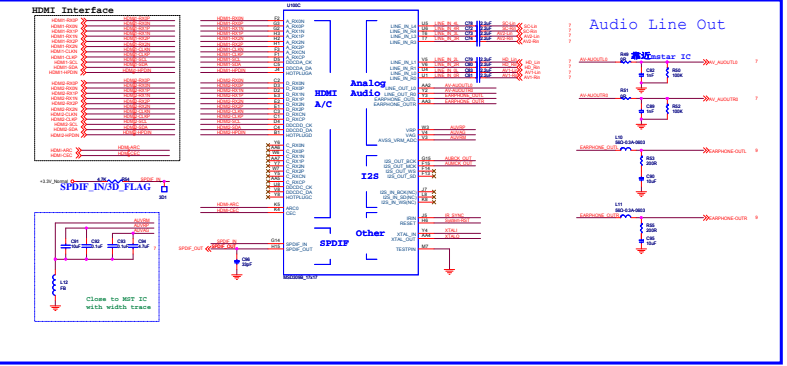
### CI & TS & Front End



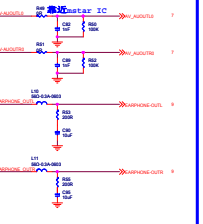
### Power interface



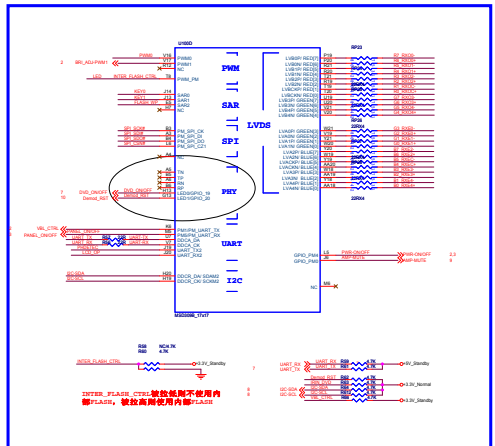
### HDMI & Audio



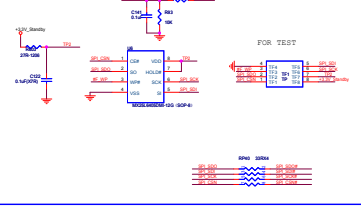
### Audio Line Out



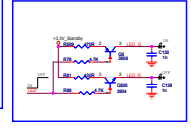
### GPIO & LVDS



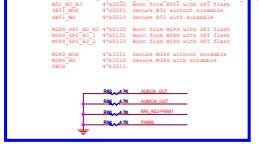
### FLASH



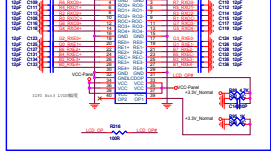
### LED



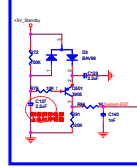
### Mode Selection



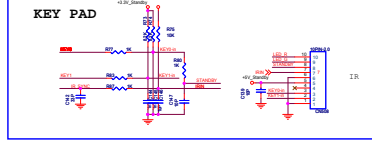
### LVDS CONNECTOR



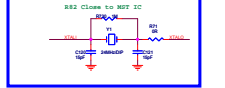
### RESET



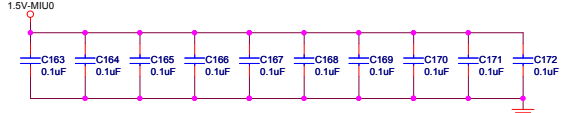
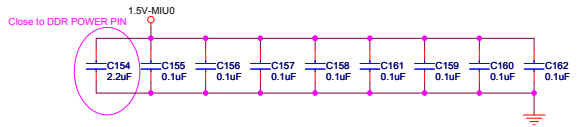
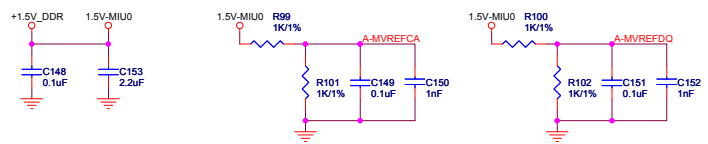
### KEYBOARD



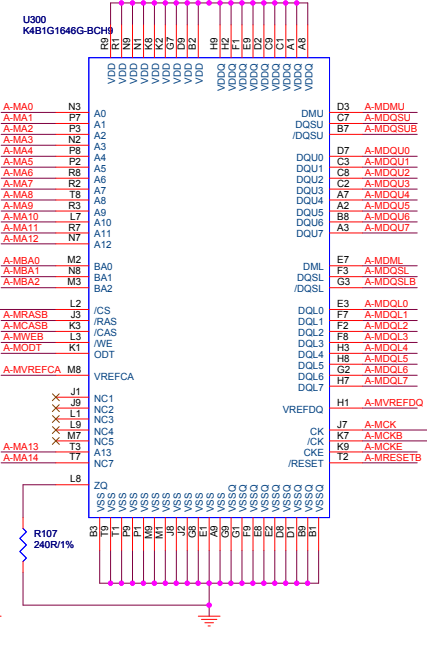
### System XTAL



AT 7000	
AT 7001	
AT 7002	
AT 7003	
AT 7004	
AT 7005	
AT 7006	
AT 7007	
AT 7008	
AT 7009	
AT 7010	
AT 7011	
AT 7012	
AT 7013	
AT 7014	
AT 7015	
AT 7016	
AT 7017	
AT 7018	
AT 7019	
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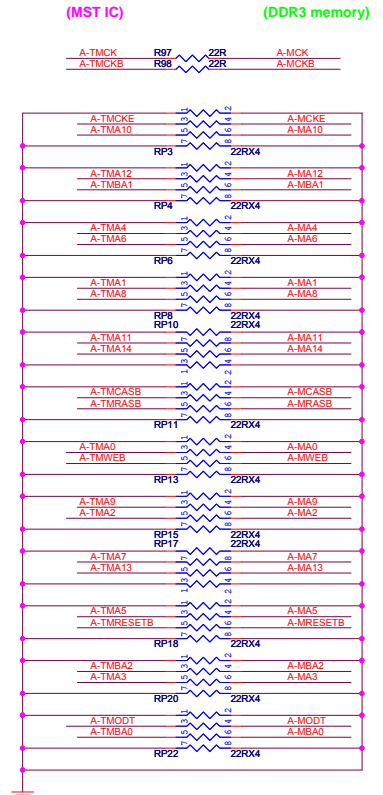
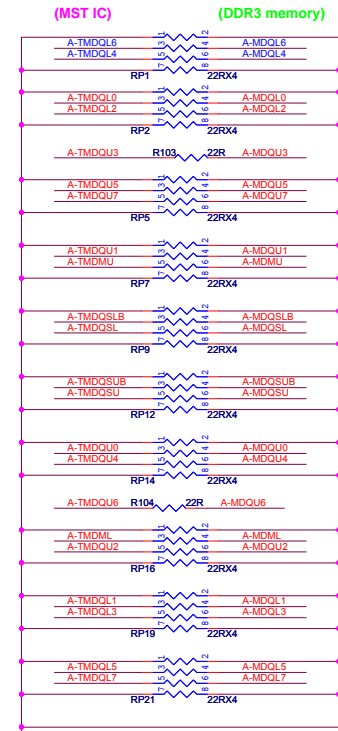


**DDR3**

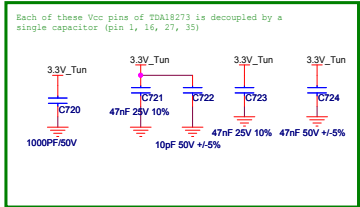
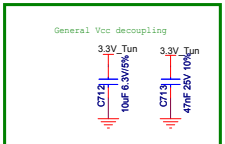
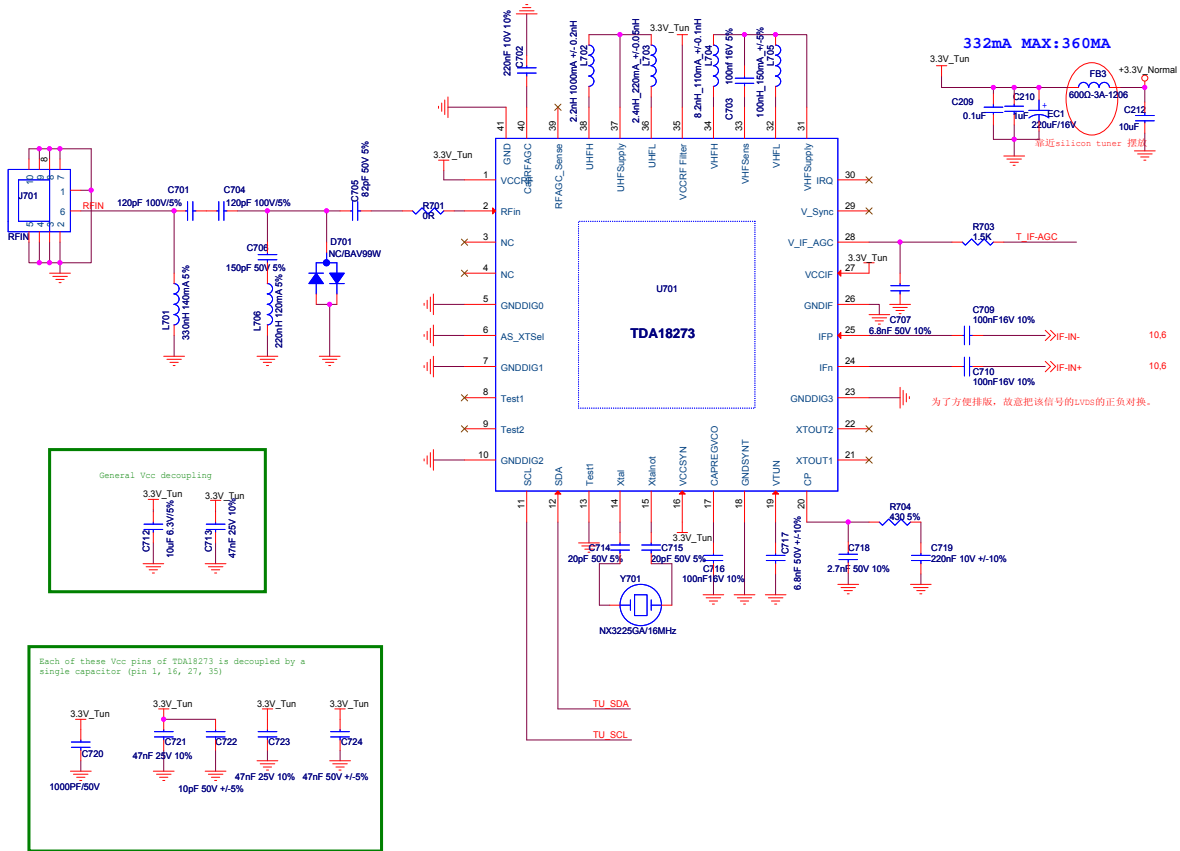


Pin	Signal	Pin	Signal	Pin	Signal
A-TMA0	C12	A_DDR3_A0/DDR2_A5	F21	A-TMDQL0	
A-TMA1	B14	A_DDR3_A1/DDR2_A13	C18	A-TMDQL1	
A-TMA2	A11	A_DDR3_A2/DDR2_A9	G19	A-TMDQL2	
A-TMA3	C10	A_DDR3_A3/DDR2_A8	B19	A-TMDQL3	
A-TMA4	C16	A_DDR3_A4/DDR2_A2	G20	A-TMDQL4	
A-TMA5	A9	A_DDR3_A5/DDR2_A3	A18	A-TMDQL5	
A-TMA6	C15	A_DDR3_A6/DDR2_A4	G21	A-TMDQL6	
A-TMA7	B10	A_DDR3_A7/DDR2_A7	B18	A-TMDQL7	
A-TMA8	A14	A_DDR3_A8/DDR2_A8	B20	A-TMDQL8	
A-TMA9	B11	A_DDR3_A9/DDR2_A14	D19	A-TMDQL9	
A-TMA10	B16	A_DDR3_A10/DDR2_RASZ	D18	A-TMDQL0	
A-TMA11	B13	A_DDR3_A11/DDR2_A6	F20	A-TMDQL1	
A-TMA12	A15	A_DDR3_A12/DDR2_A0	A20	A-TMDQL2	
A-TMA13	C11	A_DDR3_A13/DDR2_A12	E20	A-TMDQL3	
A-TMA14	C14	A_DDR3_A14/DDR2_A11	E19	A-TMDQL4	
A-TMCASB	A12	A_DDR3_CASZ/DDR2_BA0	C19	A-TMDQL5	
A-TMRASB	B12	A_DDR3_RASZ/DDR2_BA1	E18	A-TMDQL6	
A-TMWEB	C13	A_DDR3_WESZ/DDR2_A1	F19	A-TMDQL7	
A-TMODT	B8	A_DDR3_ODT/DDR2_WEZ	A17	A-TMDQL8	
A-TMBA0	A8	A_DDR3_MCLKZ/DDR2_MCLKZ	B17	A-TMDQL9	
A-TMBA1	B15	A_DDR3_DQS/DDR2_DQS0	D20	A-TMDQL0	
A-TMBA2	C9	A_DDR3_DQSLB/DDR2_DQSLB	D21	A-TMDQL1	
A-TMRESETB	B9	A_DDR3_RESET	C21	A-TMDQL2	
A-TMCKE	C17	A_DDR3_CKE/DDR2_ODT	B21	A-TMDQL3	

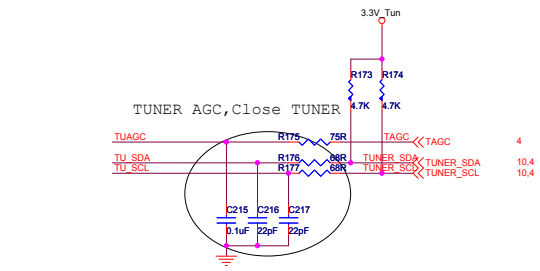
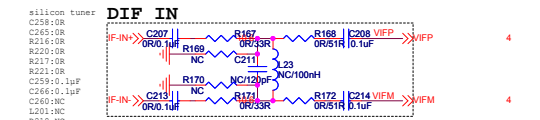
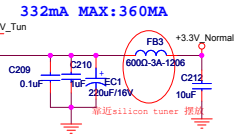
MSD306B\_17x17



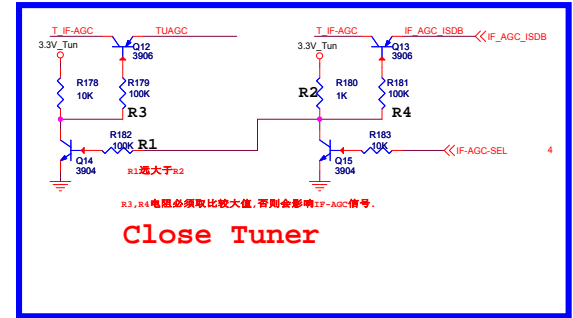
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为了方便排板, 故意把该信号的LVSS的正负对换.

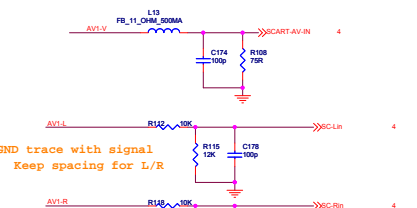
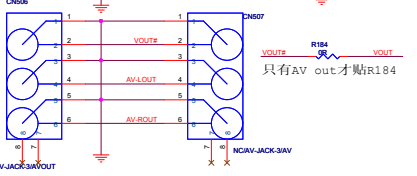
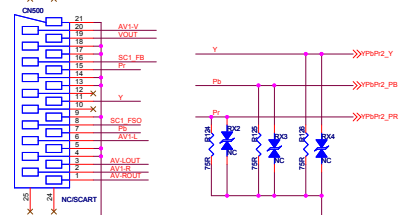
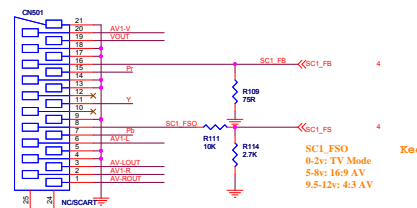


IF AGC Switch



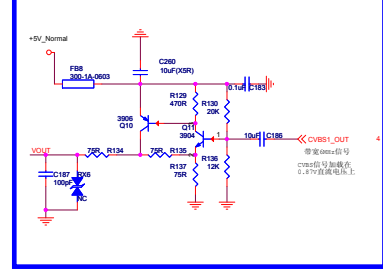
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**SCART**

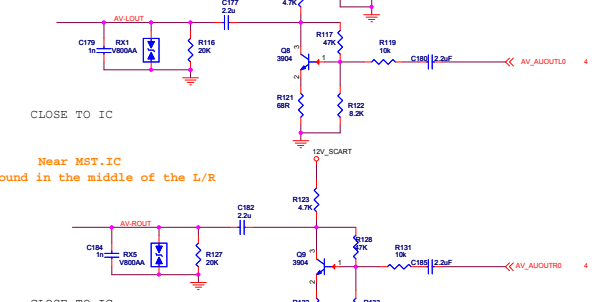


Keep AGND trace with signal  
Keep spacing for L/R

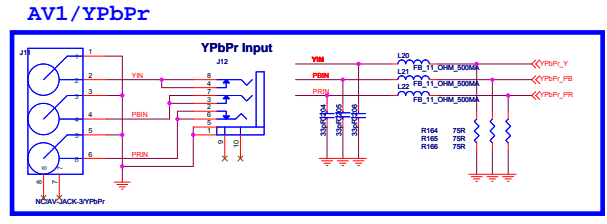
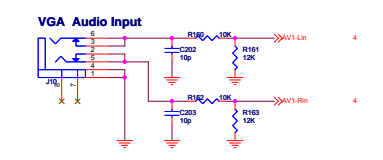
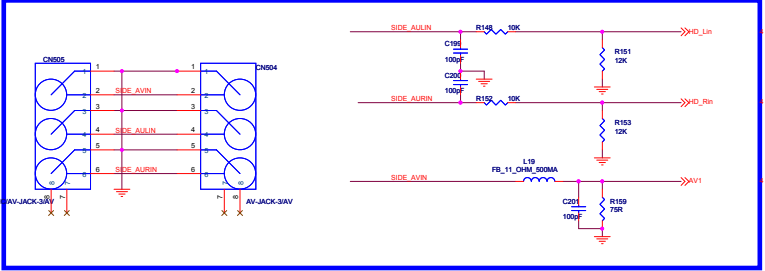
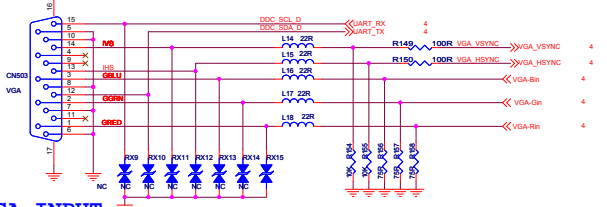
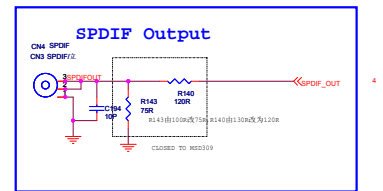
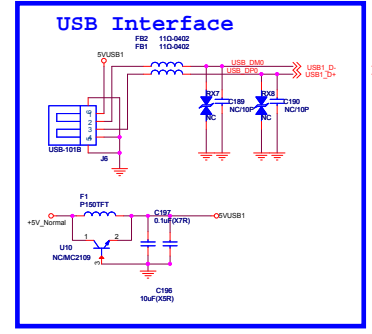
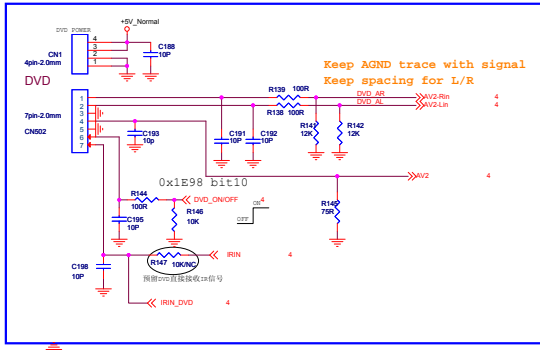
**VIDEO OUT OP**



**AUDIO\_OUTPUT**

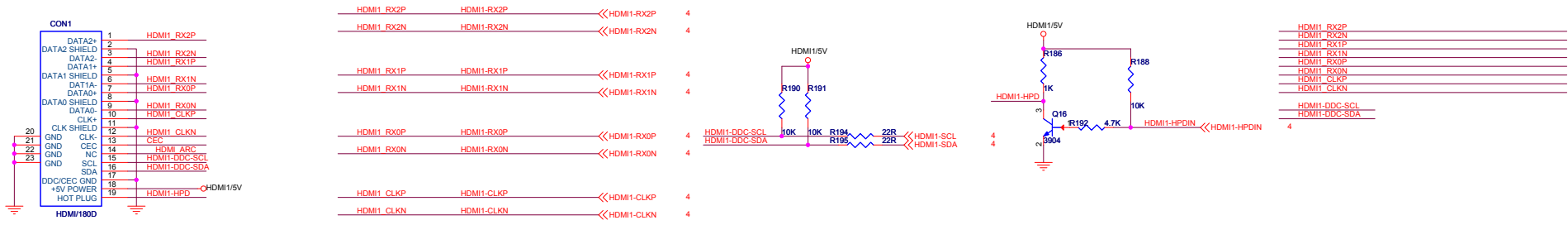


CLOSE TO IC  
Near MST.IC  
Ground in the middle of the L/R



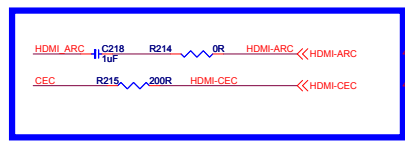
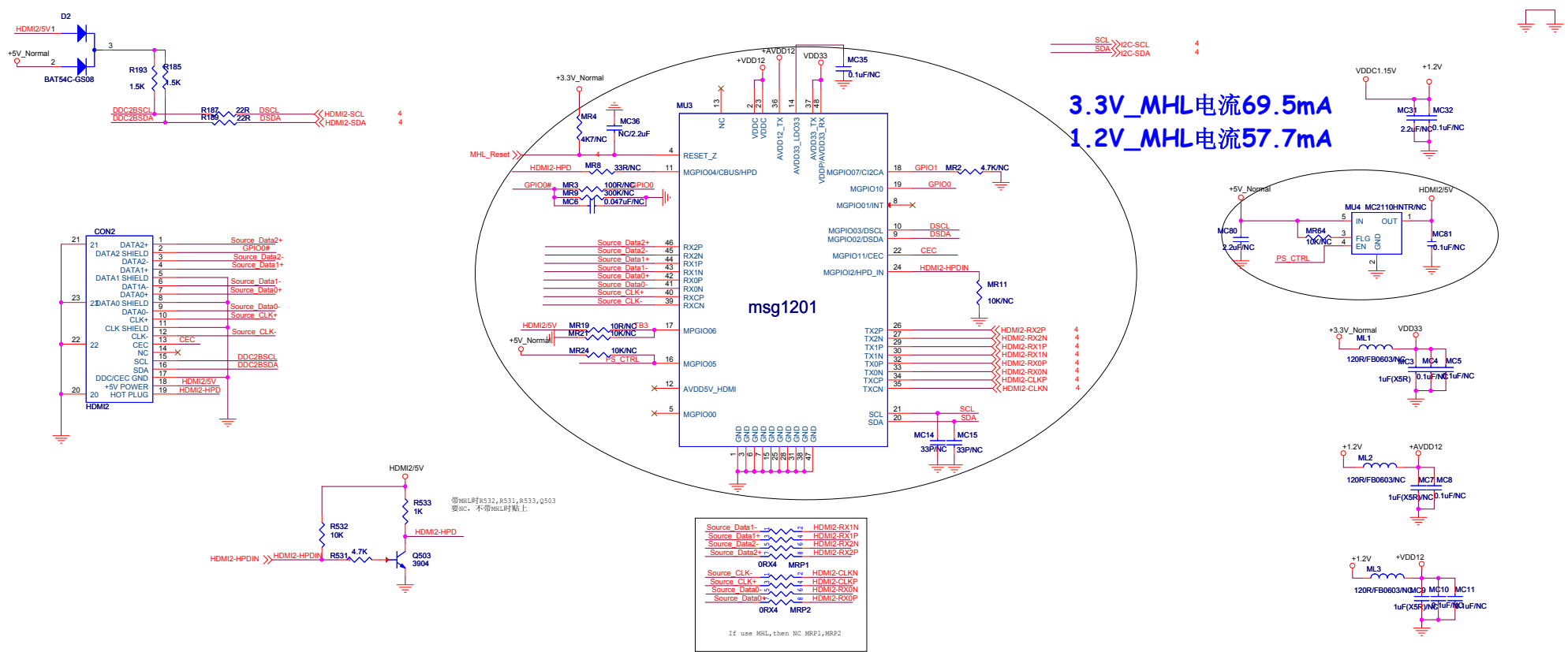
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# HDMI 1



# HDMI 2

硬件复位, 至少5ms低电平复位

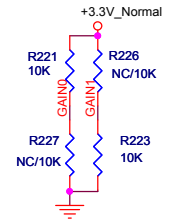
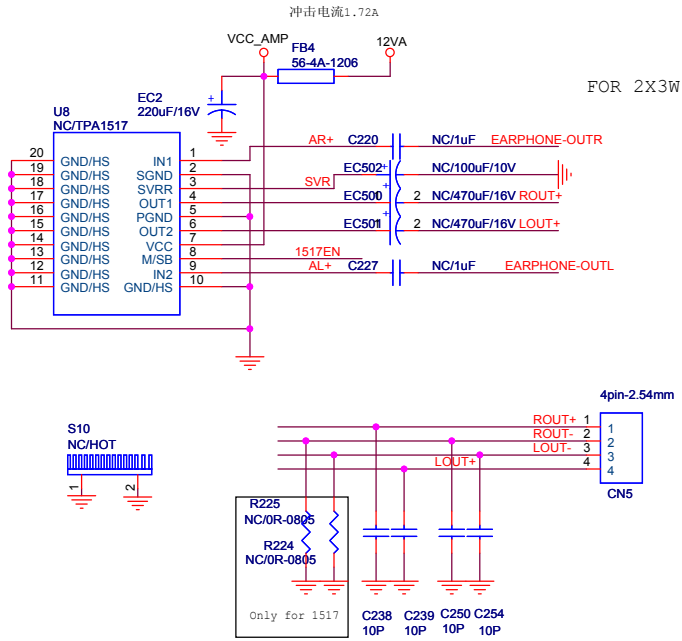


## CEC & ARC

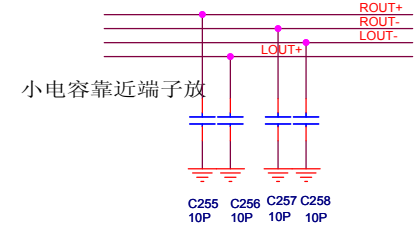
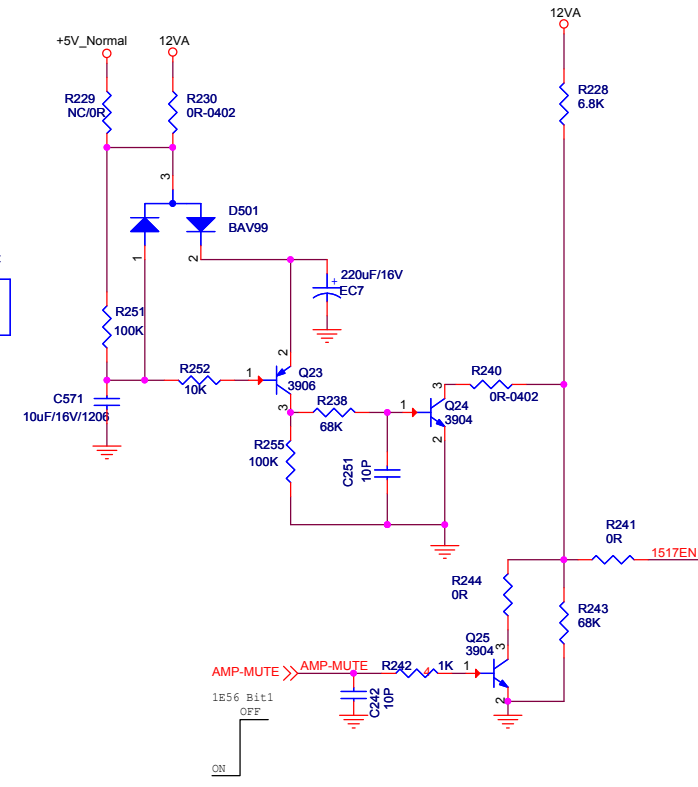
If not include MHL function, then the circle parts will be NC, and the NC components should be used.

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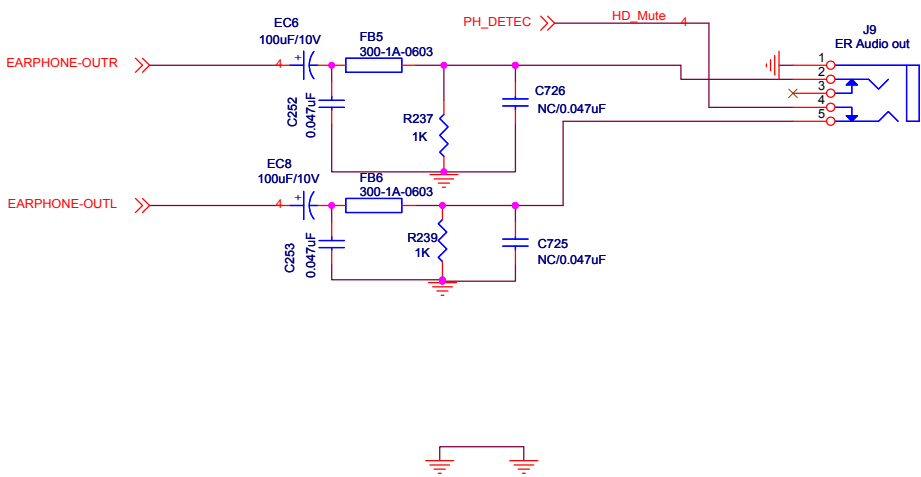




开机C571充电关机EC7放电MUTE输出  
 出电平使后级三极管导通而达到静音的目的

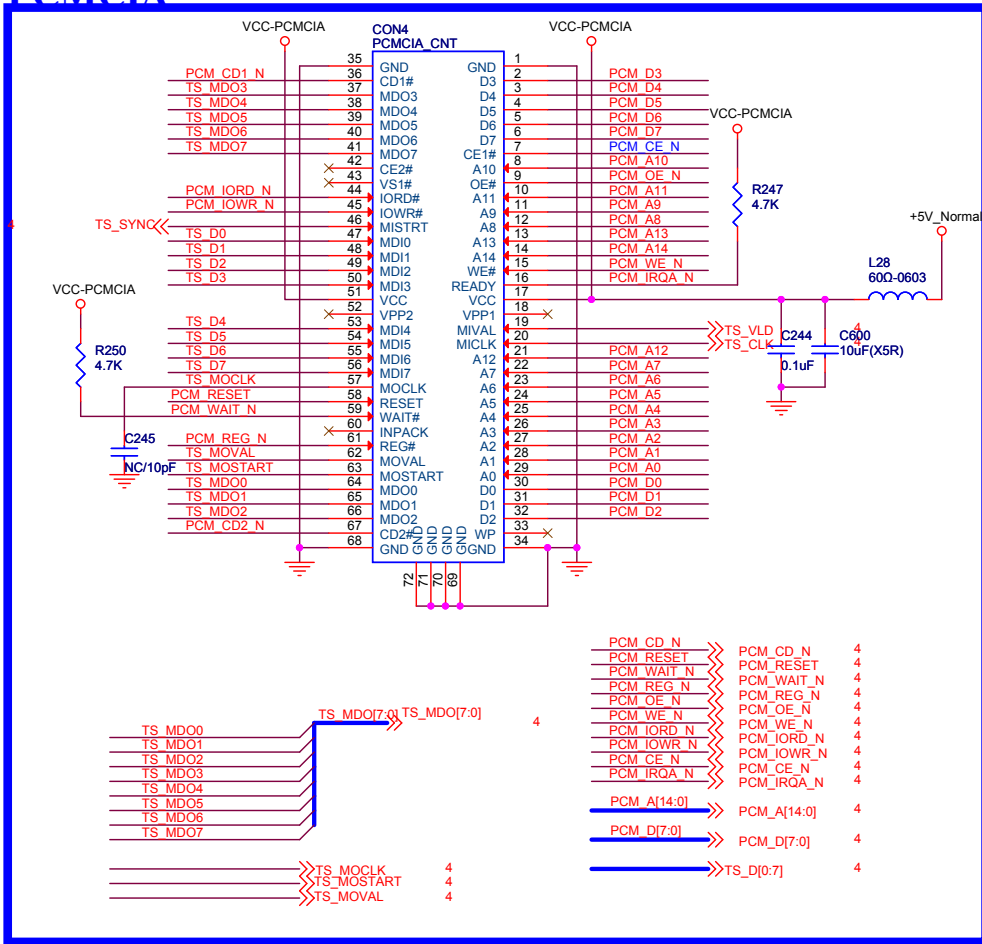


### EARPHONE OUT



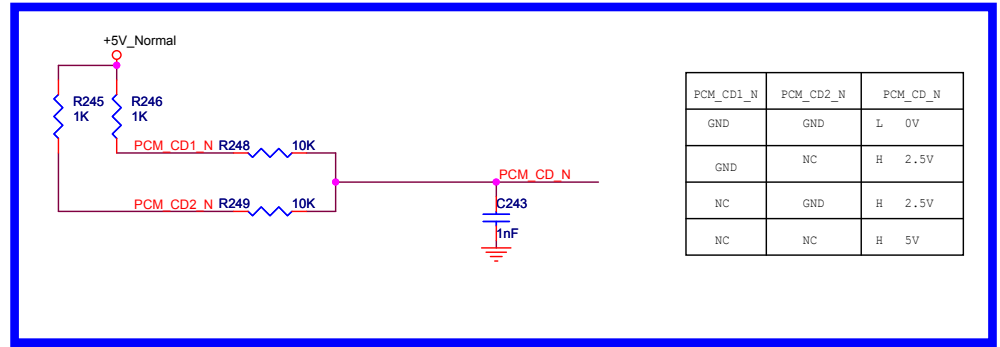
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# PCMCIA



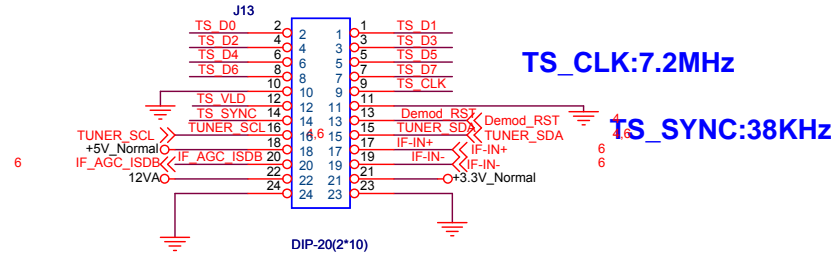
- PCM CD N >>> PCM\_CD\_N 4
- PCM RESET >>> PCM\_RESET 4
- PCM WAIT N >>> PCM\_WAIT\_N 4
- PCM REG N >>> PCM\_REG\_N 4
- PCM OE N >>> PCM\_OE\_N 4
- PCM WE N >>> PCM\_WE\_N 4
- PCM IORD N >>> PCM\_IORD\_N 4
- PCM IOWR N >>> PCM\_IOWR\_N 4
- PCM CE N >>> PCM\_CE\_N 4
- PCM IRQA N >>> PCM\_IRQA\_N 4
- PCM\_A[14:0] >>> PCM\_A[14:0] 4
- PCM\_D[7:0] >>> PCM\_D[7:0] 4
- TS\_DIO[7:0] >>> TS\_DIO[7:0] 4
- TS\_MOCLK >>> TS\_MOCLK 4
- TS\_MOSTART >>> TS\_MOSTART 4
- TS\_MOVAL >>> TS\_MOVAL 4

# CARD DETECT



PCM_CD1_N	PCM_CD2_N	PCM_CD_N
GND	GND	L 0V
GND	NC	H 2.5V
NC	GND	H 2.5V
NC	NC	H 5V

# DEMO SOCK



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